

DVB-C Annex-A (Cable TV) Receiver, TS Recorder, DVB-ASI/SPI Player, DVB-C to DVB-ASI/SPI Converter

Standard Features:

- Combined DVB-C Input and DVB-ASI/SPI Output Board.
- PCI 2.2, 32 bit, 33/66MHz 3.3V.
- Bus Master DMA, Scatter /Gather Interface Protocol.
- Windows 2000 & XP Drivers and now Linux Drivers & API.
- Accompanied by DVStation2 Alitronika's Application Software.

Input

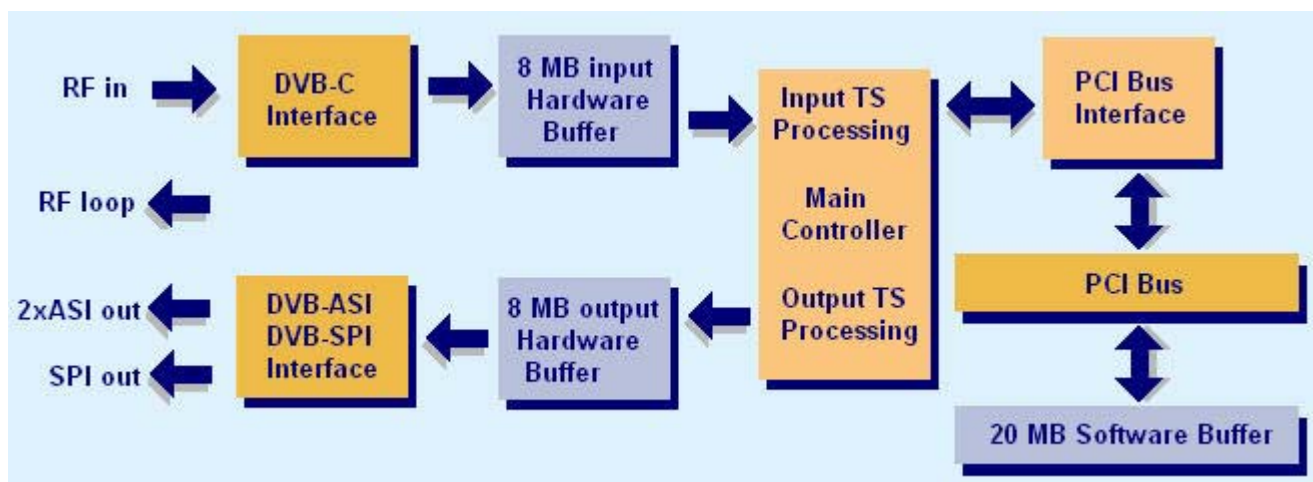
- DVB-C Compliant QAM Reception.
- Input Frequency Range: 47 MHz to 862 MHz.
- Symbol Rate: Variable capability from SACLK/64 to SACLK/4
- Supports 188, 204 Transport Packet Sizes.

Output

- RF Loop Through Output.
- Simultaneous two Serial (ASI) and one Parallel (SPI) Outputs.
- Supports DVB According to Standard A1010 Rev1 and EN50083.



Block Diagram:



AT700PCI

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Applications:

Targeted for Digital Video Professionals, Sophisticated End Users and OEMs

The AT700PCI is an ideal solution for a number of applications such as:

- Development Tools.
- DVB to IP Gateway.
- DVB-C to DVB-ASI & DVB-SPI Converter.
- MPEG-II Transport Stream Recording, Monitoring, Playing & Processing.
- Software Based MPEGII Decoders.

Specifications:

- On Board Buffer: 16 Mbytes.
- RF Tuner Connector: 75 Ohms IEC female Type.
- Loop Through Connector: 75 Ohms IEC male Type.
- Input Frequency Range: 47 MHz to 862 MHz.
- Input Level: -15dBmV to +15dBmV.
- LO Step Size: 62.5 KHz.
- Standards: DVB-C (ETS 300-429/ITU-T J83 Annex A).
- Modulation Modes: 4/16/32/64/128 QAM.
- DVB-ASI Connector: 75 Ohms BNC.
- Output Signal level: 1.0Vp-p nominal.
- DVB-SPI Connector: 25-pin sub-D.
- DVB-ASI Output Bit Rate: 0 to 214 Mbit/s.
- DVB-SPI Output Bit Rate: 0 to 108 Mbit/s.
- DVB-ASI Output Clock: 270 MHz.
- DVB-SPI Output Clock: 0 to 13.5 MHz.
- DVB-SPI Output Level: LVDS.
- Size WxL: 107mm x 175mm.

IP Solutions Ltd. www.ip-solutions-ltd.com

Tel: +852 2850 8383 Fax: +852 2850 8182 E-mail: sales@ip-solutions-ltd.com

Rm1704, Lucky Commercial Centre, 103 Des Voeux Road West, Sheung Wan, Hong Kong